

DAQ Status

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New For Run 5

(from last month's report with this month's status)

- MUTR Multievent Buffering Works, as far as we know; major rewrite of FPGA Data taken with noise trigger; counters correlated with GL1
- EMCAL Multievent Buffering Might work; pretty much complete rewrite of all three FPGA's Looking ok; now used in standard feed
- JSEB firmware updated to eliminate padding banks; seems to work... More dead JSEB's than I'd like, but padding still gone
- Smart Trigger Pre-Processors added to 6 MUTR DCM crates Hasn't shown any problems
- Six Buffer Boxes The order should be out soon
- Programmable Pulse Generator replaced by Mode Bit Driver Laser data taken that way; seems ok; I'm not aware of any BB data taken that way
- Split Level 1 data into GL1+LL1 We've run this, but we still have some testing to do on it
- New granule hardware: AGEL; TOF.W? AGEL done AFAIK, TOF.W getting done this week
- Computers all upgraded to Scientific Linux 3.0.2 Not all; most
- Event Builder now based on Linux Testing in progress

The Coming Days

- Build up the Big Partition; we haven't yet tried a really sizable partition, but David has run the majority of SEB's now
- TOF.W appeared a couple of weeks ago, and we have to put it together and turn it into a running granule
- We need to finish the split of the GL1 and LL1 data; Jamie has it basically working, but we need to add the rest of the LL1's and do some more checking. It's disruptive to other testing, so we just have to make time for it, probably next week
- Buffer Boxes and disk arrays are likely to arrive while we're running; could be somewhat disruptive; we ordered a couple of new modules for the Gigabit switch, so we'll probably want to shut it down when we get them
- We need to complete the transition of the Run database to PostgreSQL; Ed has been working on the rc part
- There are no doubt many bugs and operational gotchas that will have to be fixed in rc and the servers before the shift crews can use them routinely

Other Issues

- Does the MVD work with multievent buffering? The answer is not clear. When we bring the MVD up, we can do a test with the random trigger that should shed some light on that.
- TEC noise. Achim and the TEC guys have made a lot of progress on this; we probably have to do some work to reduce noise from ARCNET.
- I think we resolved an annoying problem that developed with the LV and Rack control when we went to GraphWorX32 7.0.
- Need to do something with CORBA services (now running on a run-down junker)
- Switching versions to bring in new IDL, DCM software, etc.
- STAR scalers
- Beam activities starting November 22 once again neatly meshes with the holidays